

Inventor: Garo J. Derderian et al  
Title: Capacitor Fabrication Methods and Capacitor  
Constructions  
Assignee: Micron Technology, Inc.  
Priority Serial No.: 09/652,532  
Priority Filing Date: August 31, 2000  
Docket No. MI22-1752



**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

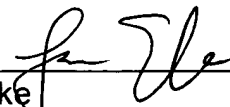
In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a divisional application of co-pending application Serial No. 09/652,532, filed August 31, 2000. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. § 1.98(d) and MPEP § 609(2).

Citation of these references is respectfully requested.

Respectfully submitted;

Dated: 14 June 2001

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